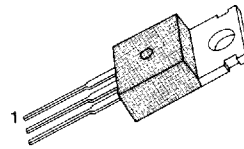


FEATURES

- Lower $R_{DS(ON)}$
- Improved inductive ruggedness
- Fast switching times
- Rugged polysilicon gate cell structure
- Lower input capacitance
- Extended safe operating area
- Improved high temperature reliability

TO-220



1. Gate 2. Drain 3. Source

PRODUCT SUMMARY

Part Number	V _{DS}	R _{DS(on)}	I _D
IRF720	400V	1.8Ω	3.3A
IRF721	350V	1.8Ω	3.3A

ABSOLUTE MAXIMUM RATINGS

Characteristic	Symbol	IRF720	IRF721	Unit
Drain-Source Voltage (1)	V _{DSS}	400	350	Vdc
Drain-Gate Voltage (R _{GS} =1.0MΩ)(1)	V _{DGR}	400	350	Vdc
Gate-Source Voltage	V _{GS}	± 20		Vdc
Continuous Drain Current T _C =25 °C	I _D	3.3		Adc
Continuous Drain Current T _C =100 °C	I _D	2.1		Adc
Drain Current - Pulsed (3)	I _{DM}	13		Adc
Gate Current - Pulsed	I _{GM}	± 1.5		Adc
Single Pulsed Avalanche Energy (4)	E _{AS}	190		mJ
Avalanche Current	I _{AS}	3.3		A
Total Power Dissipation @ T _C =25 °C	P _D	50		Watts
Derate above 25 °C		0.40		
Operating and Storage	T _J , T _{STG}	-55 to +150		°C
Junction Temperature Range				
Maximum Lead Temp. for Soldering	T _L	300		°C
Purposes, 1/8" from case for 5 seconds				

Notes : (1) T_J=25°C to 150°C

(2) Pulse test : Pulse width ≤ 300μs, Duty Cycle ≤ 2%

(3) Repetitive rating : Pulse width limited by max. junction temperature

(4) L=31mH, V_{DD}=50V, R_G=25Ω, Starting T_J=25°C

ELECTRICAL CHARACTERISTICS (Tc=25°C unless otherwise specified)

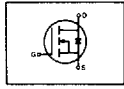
Symbol	Characteristic	Min	Typ	Max	Units	Test Conditions
BV _{DSS}	Drain-Source Breakdown Voltage					
	IRF720	400	-	-	V	V _{GS} =0V, I _D =250μA
	IRF721	350	-	-	V	
V _{GS(th)}	Gate Threshold Voltage	2.0	-	4.0	V	V _{DS} =V _{GS} , I _D =250μA
I _{GSS}	Gate-Source Leakage Forward	-	-	100	nA	V _{GS} =20V
I _{GSS}	Gate-Source Leakage Reverse	-	-	-100	nA	V _{GS} =-20V
I _{DSS}	Zero Gate Voltage Drain Current	-	-	250	μA	V _{DS} =Max. Rating, V _{GS} =0V
		-	-	1000	μA	V _{DS} =0.8 Max. Rating, V _{GS} =0V, Tc=125°C
R _{DS(on)}	Static Drain-Source On Resistance(2)	-	1.4	1.8	Ω	V _{GS} =10V, I _D =1.7A
g _{fs}	Forward Transconductance (2)	1.0	2.2	-	∩	V _{DS} ≥50V, I _D =1.7A
C _{iss}	Input Capacitance	-	400	-	pF	V _{GS} =0V, V _{DS} =25V, f=1.0MHz
C _{oss}	Output Capacitance	-	59.3	-	pF	
C _{rss}	Reverse Transfer Capacitance	-	27	-	pF	
t _{d(on)}	Turn-On Delay Time	-	10	15	ns	V _{DD} =0.5 BV _{DSS} , I _D =3.3A, Z _θ =18Ω (MOSFET switching times are essentially independent of operating temperature)
t _r	Rise Time	-	14	20	ns	
t _{d(off)}	Turn-Off Delay Time	-	30	45	ns	
t _f	Fall Time	-	13	20	ns	
Q _g	Total Gate Charge (Gate-Source Plus Gate-Drain)	-	-	25	nC	V _{GS} =10V, I _D =3.3A, V _{DS} =0.8 Max. Rating (Gate charge is essentially independent of operating temperature)
Q _{gs}	Gate-Source Charge	-	3.0	-	nC	
Q _{gd}	Gate-Drain ("Miller") Charge	-	11	-	nC	

THERMAL RESISTANCE

Symbol	Characteristics		All	Units	Remark
R _{thJC}	Junction-to-Case	MAX	2.5	K/W	
R _{thCS}	Case-to-Sink	TYP	0.5	K/W	Mounting surface flat, smooth, and greased
R _{thJA}	Junction-to-Ambient	MAX	62.5	K/W	Free Air Operation

- Notes : (1) T_J=25°C to 150°C
 (2) Pulse test : Pulse width ≤ 300μs, Duty Cycle ≤ 2%
 (3) Repetitive rating : Pulse width limited by max. junction temperature

SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS

Symbol	Characteristic	Min	Typ	Max	Units	Test Conditions
I_S	Continuous Source Current (Body Diode)	-	-	3.3	A	Modified MOSFET symbol showing the integral reverse P-N junction rectifier 
I_{SM}	Pulse Source Current (Body Diode) (3)	-	-	13	A	
V_{SD}	Diode Forward Voltage (2)	-	-	1.8	V	$T_J=25^\circ\text{C}$, $I_S=3.3\text{A}$, $V_{GS}=0\text{V}$
t_{rr}	Reverse Recovery Time	-	270	-	ns	$T_J=25^\circ\text{C}$, $I_F=3.3\text{A}$, $dI_F/dt=100\text{A}/\mu\text{S}$

- Notes : (1) $T_J=25^\circ\text{C}$ to 150°C
 (2) Pulse test : Pulse width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$
 (3) Repetitive rating : Pulse width limited by max. junction temperature

